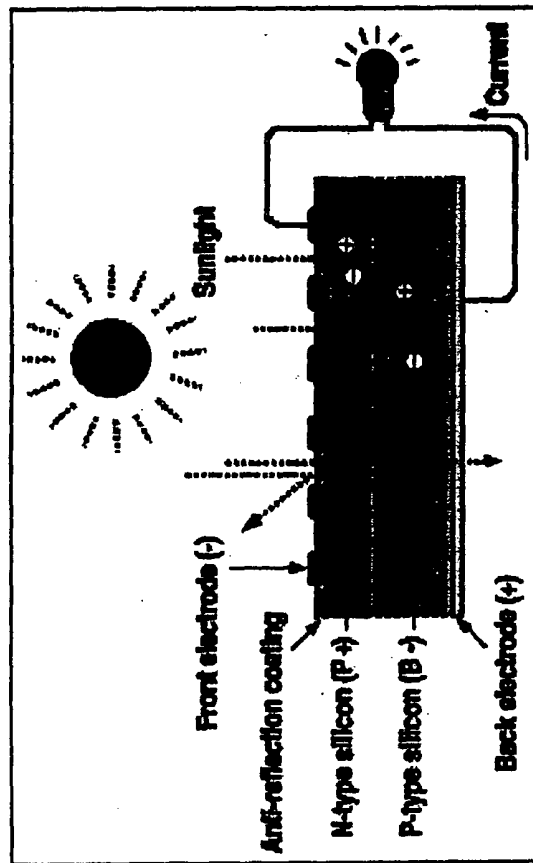
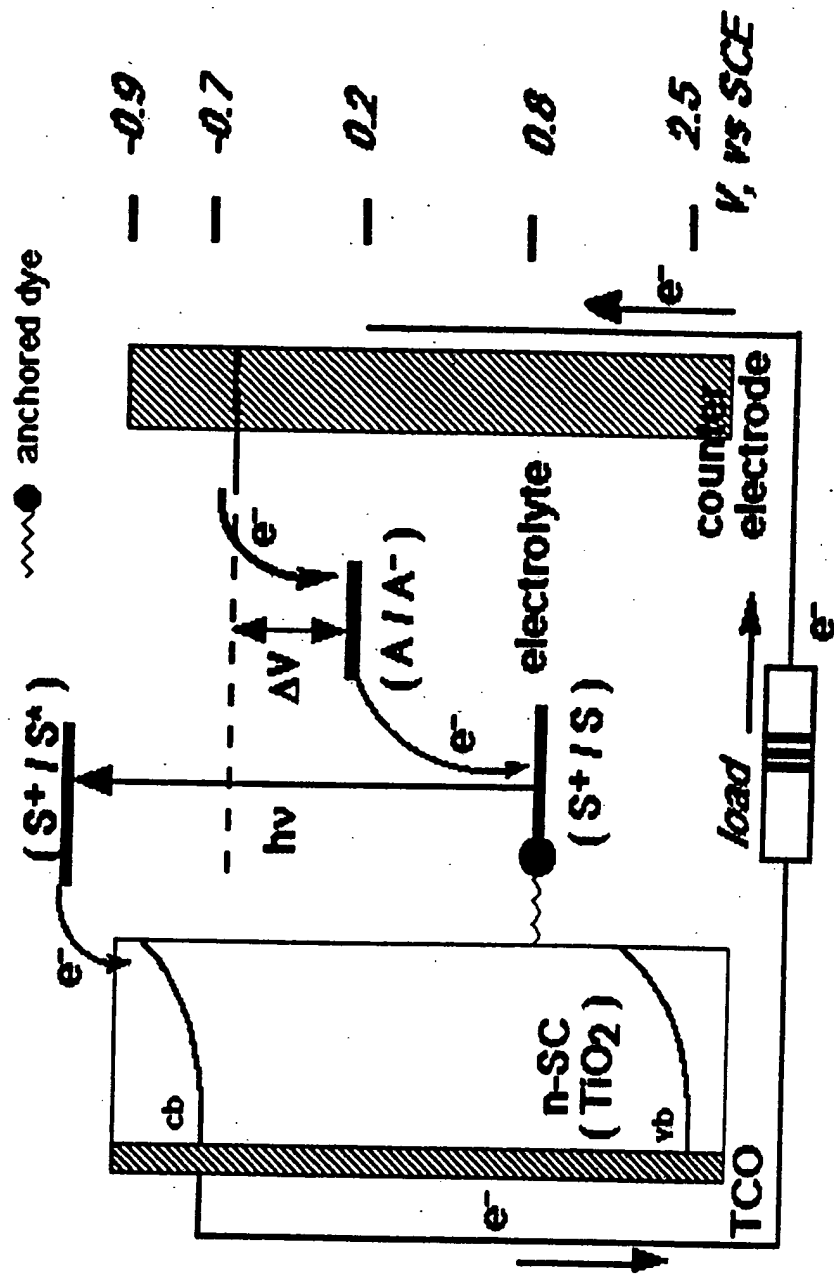


Figure 1



Prior Art

Figure 2



Prior Art

Figure 3

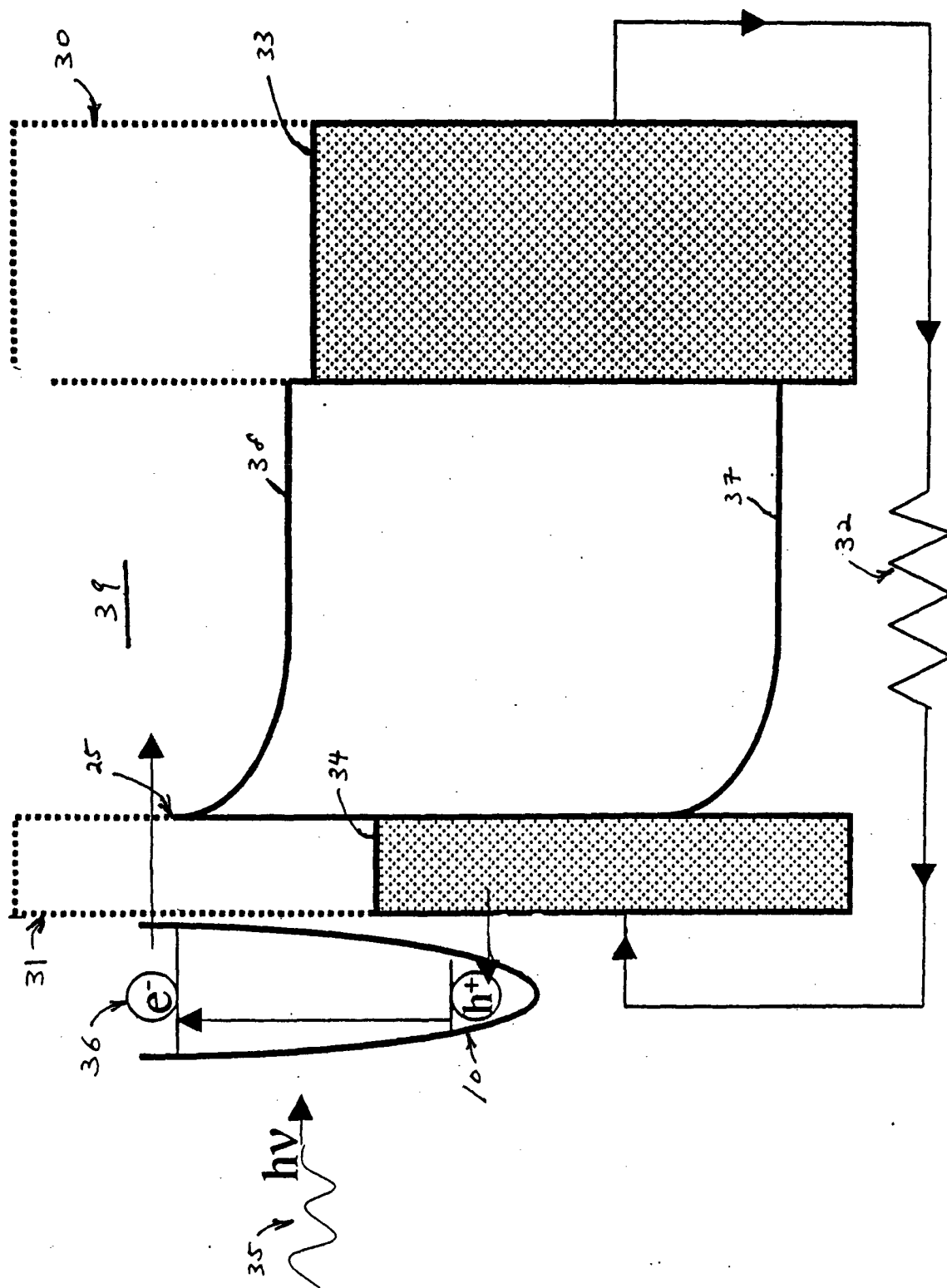


Figure 4

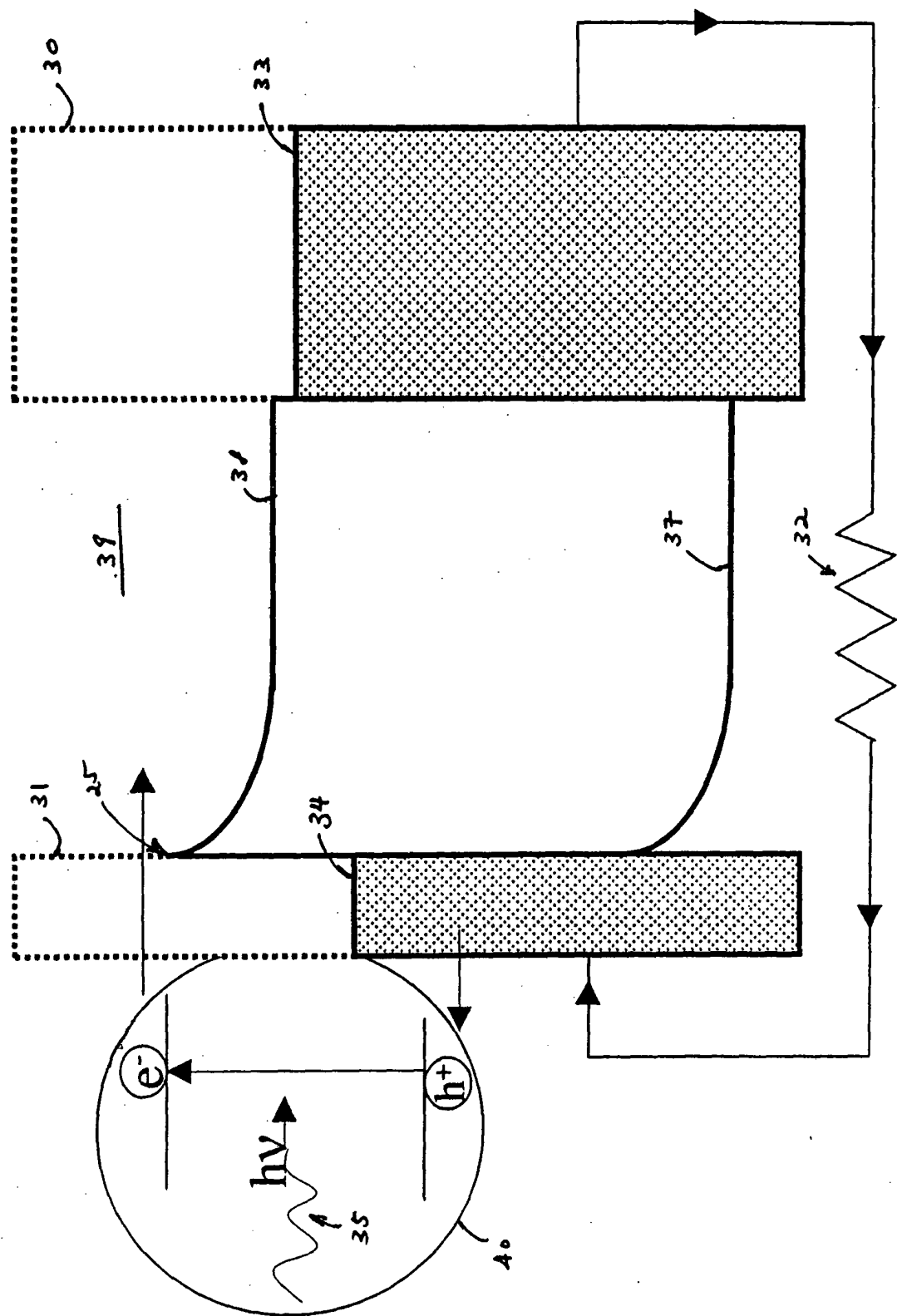


Figure 5

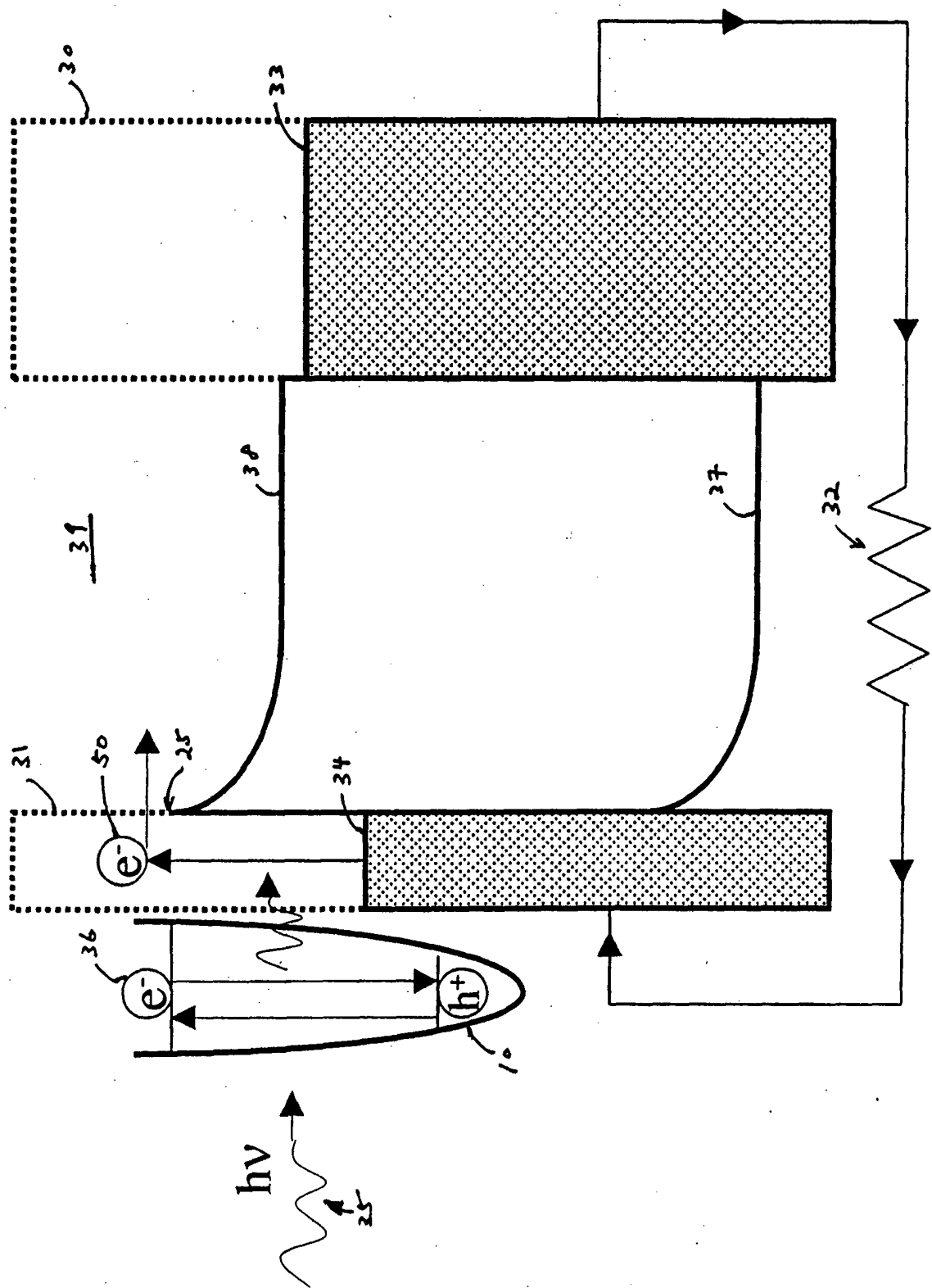


Figure 6

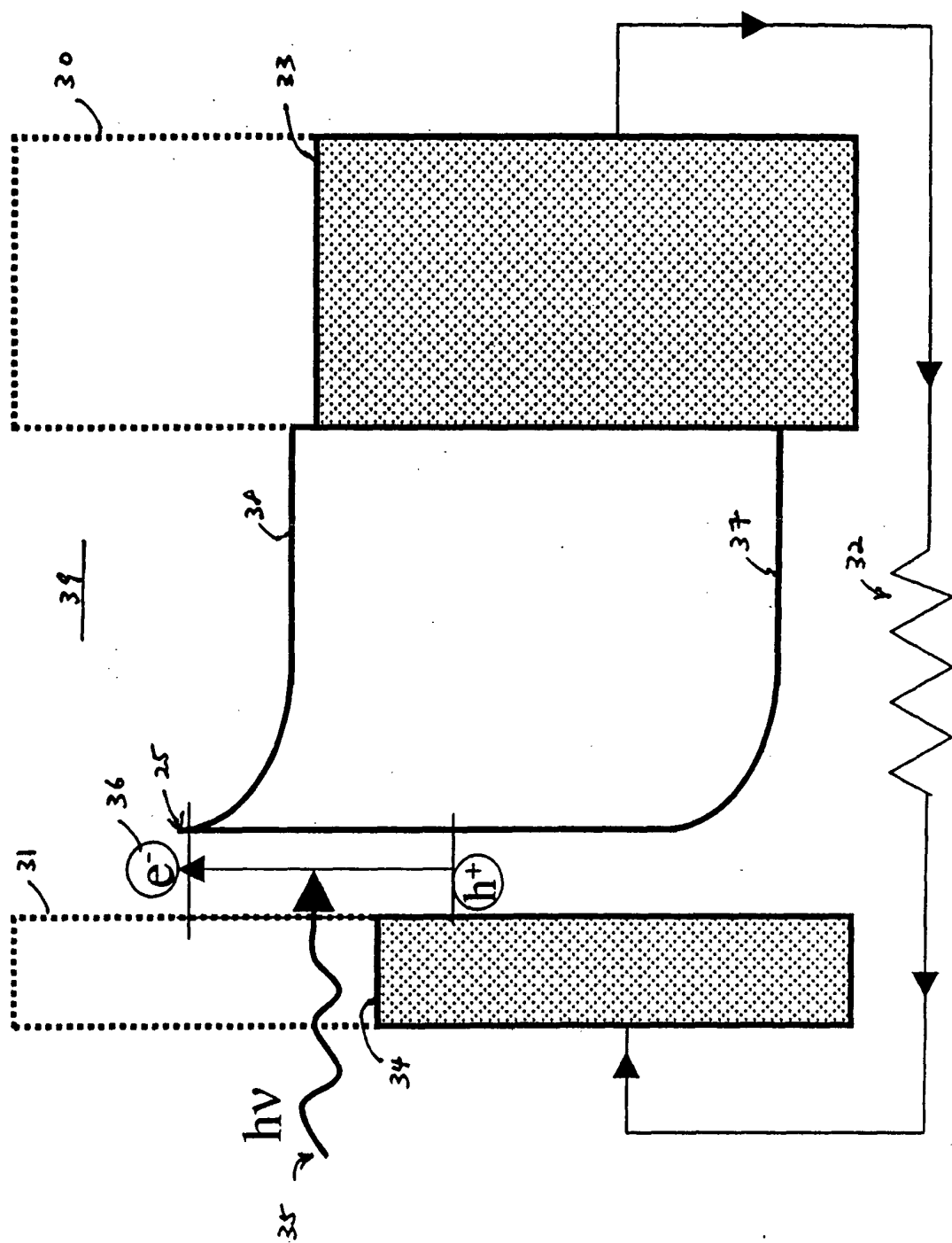


Figure 7

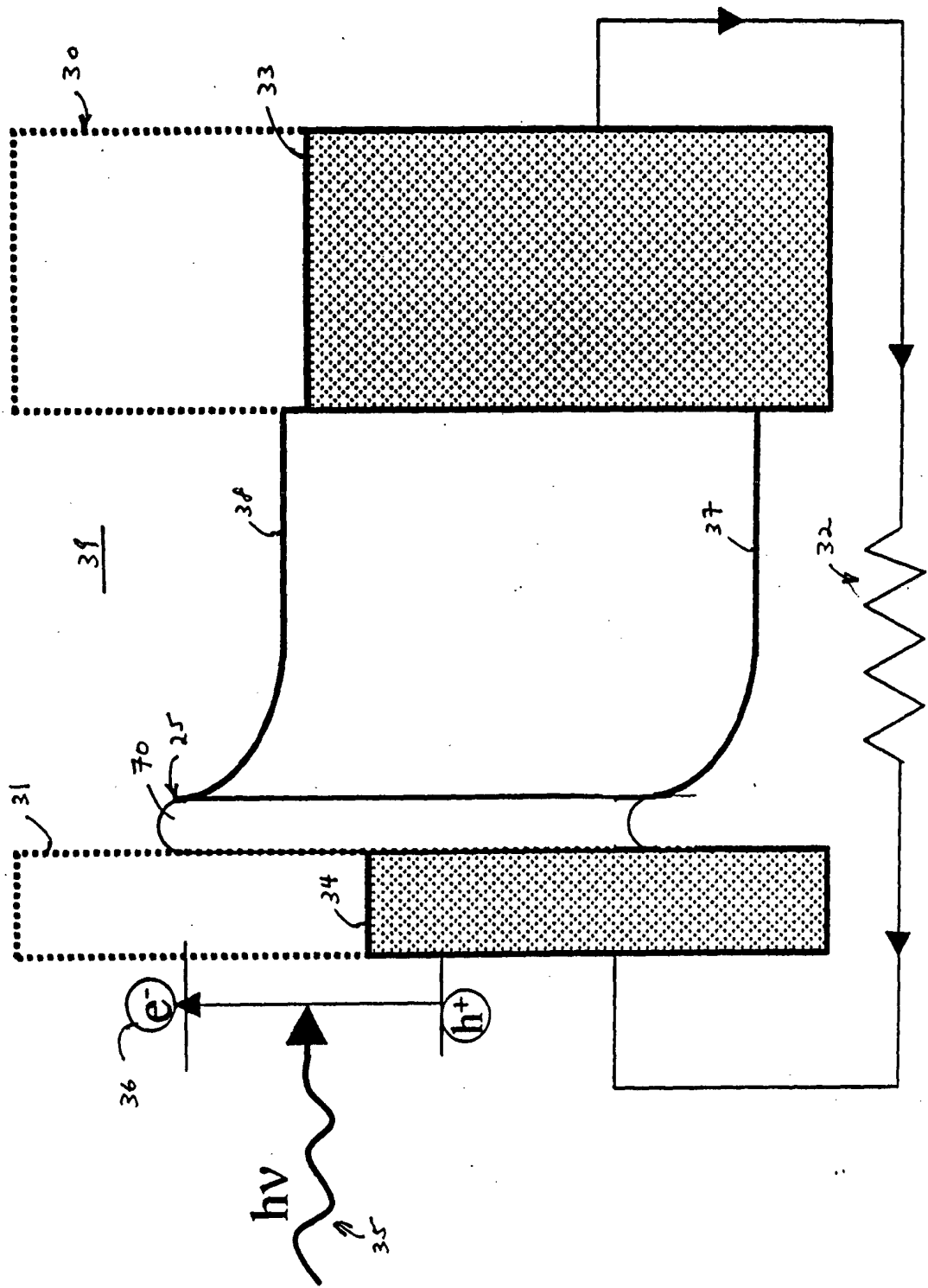
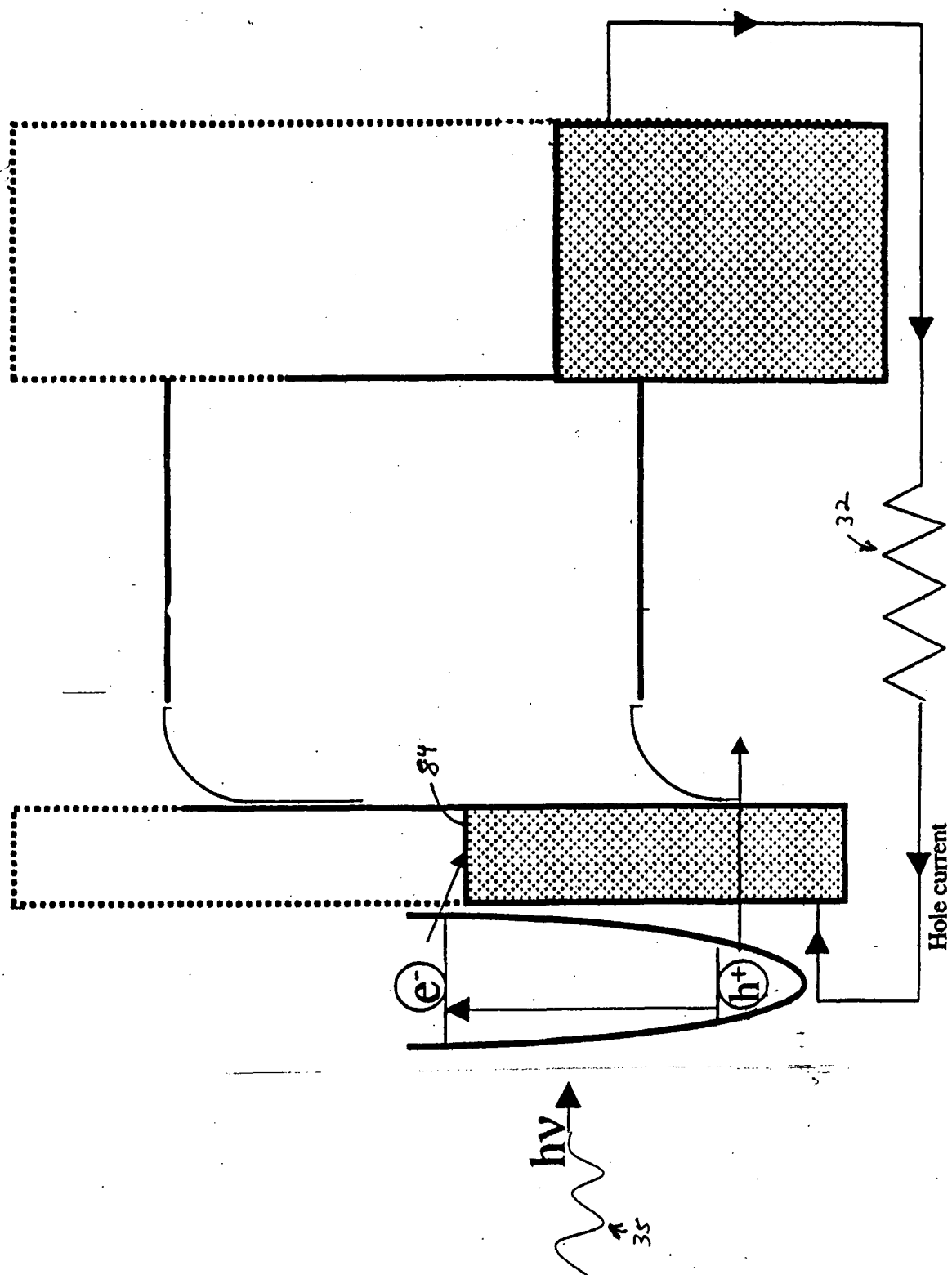


Figure 8





## Figure 9

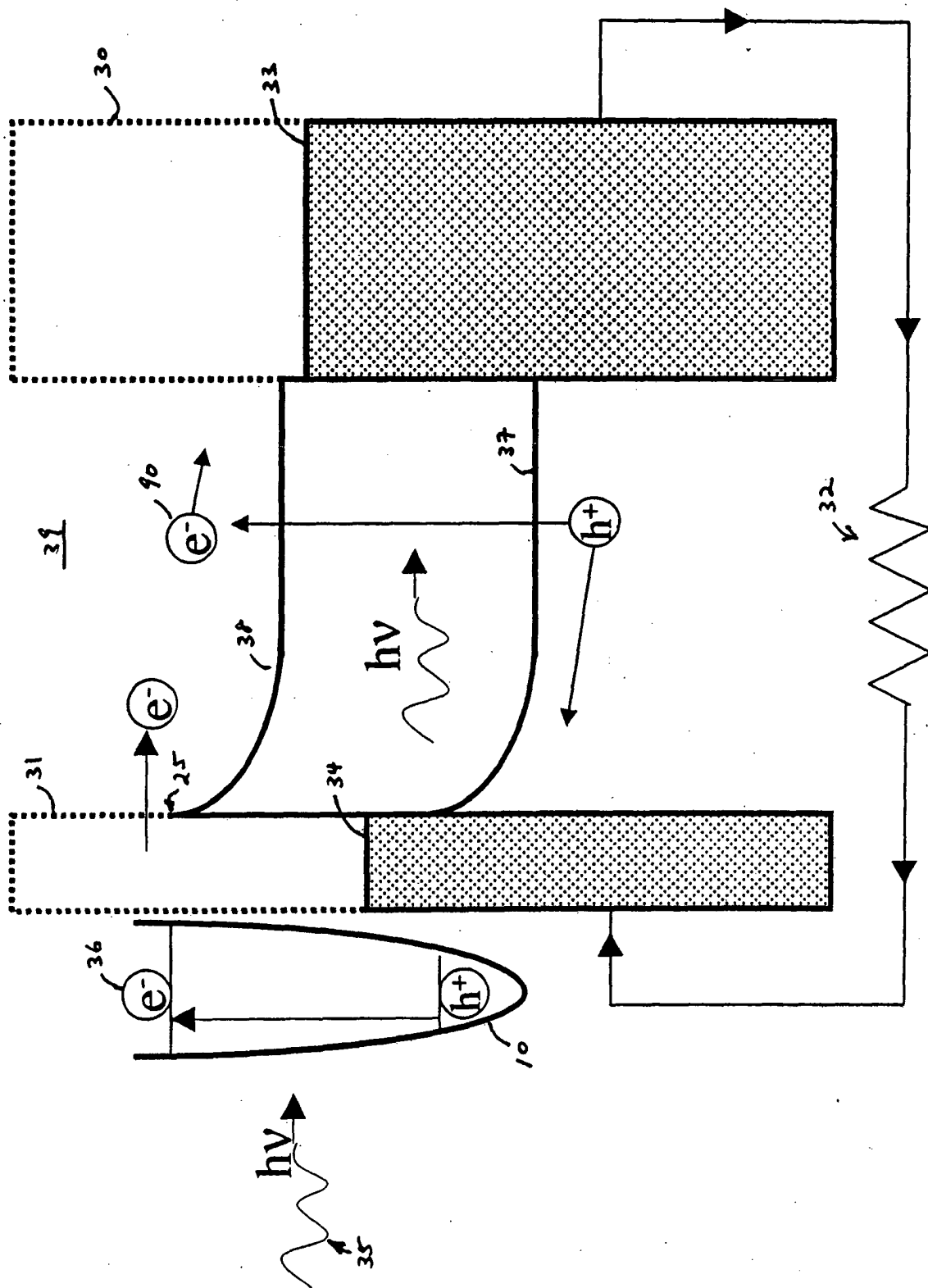


Figure 10

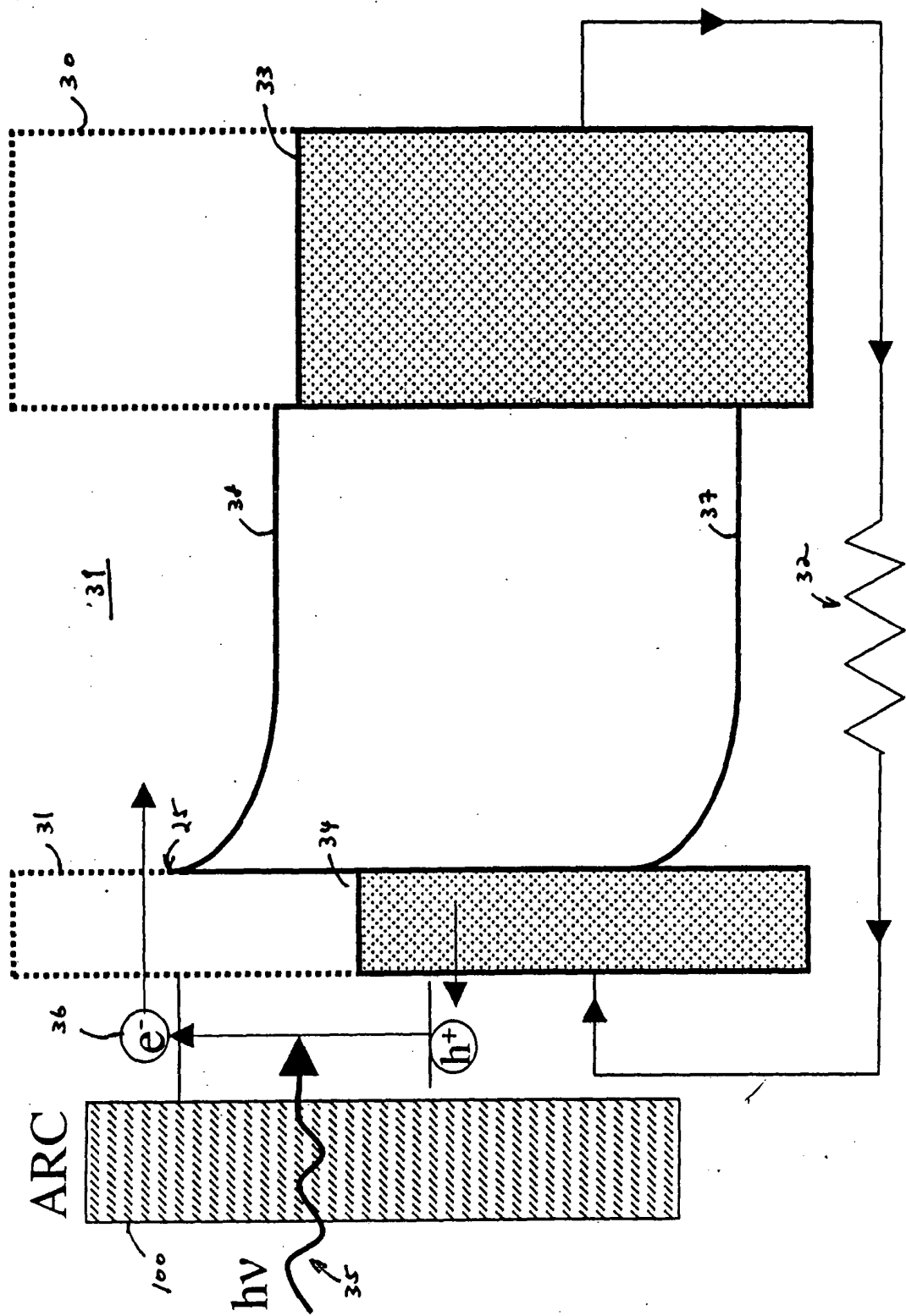


Figure 11:

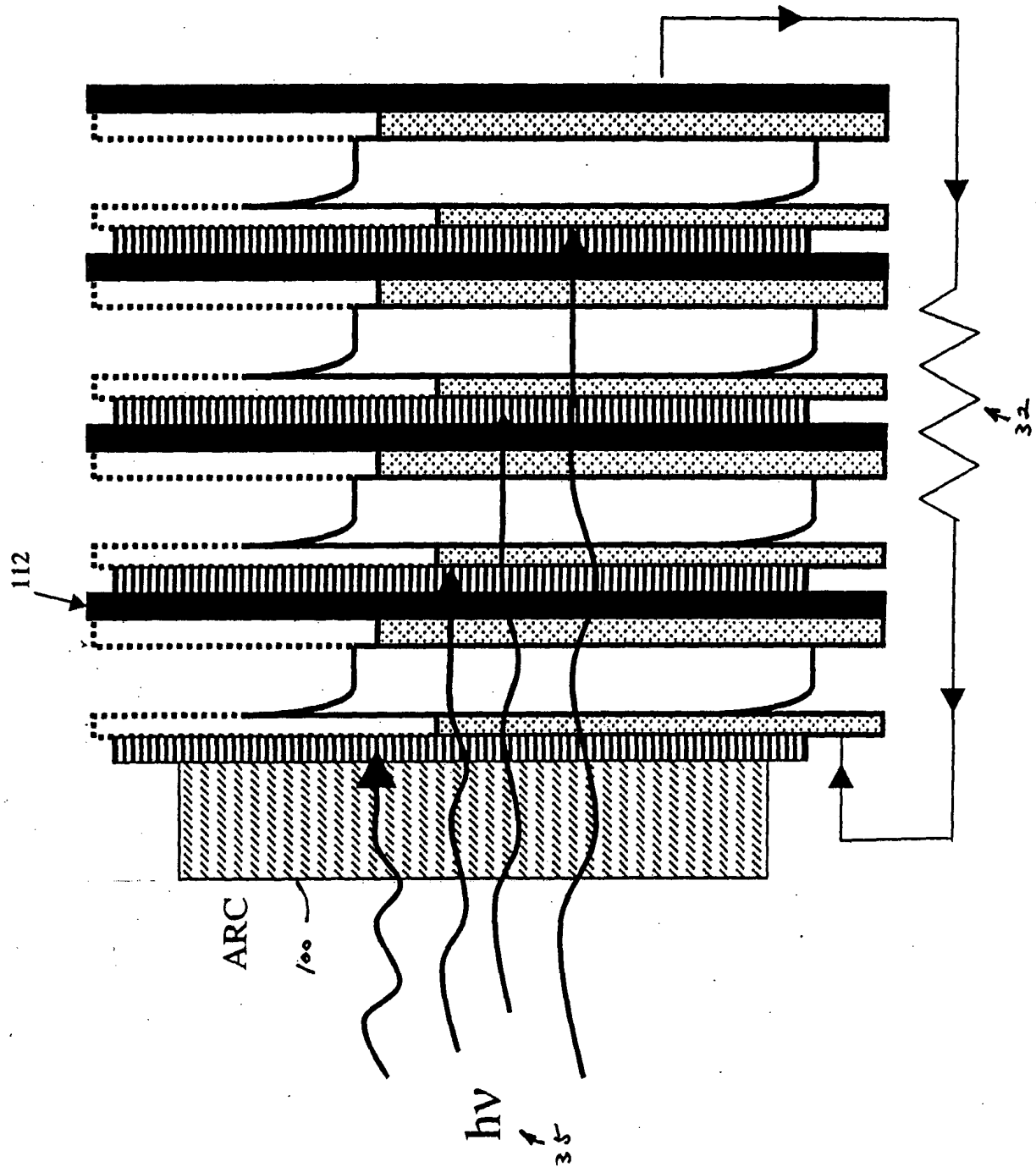


Figure 12

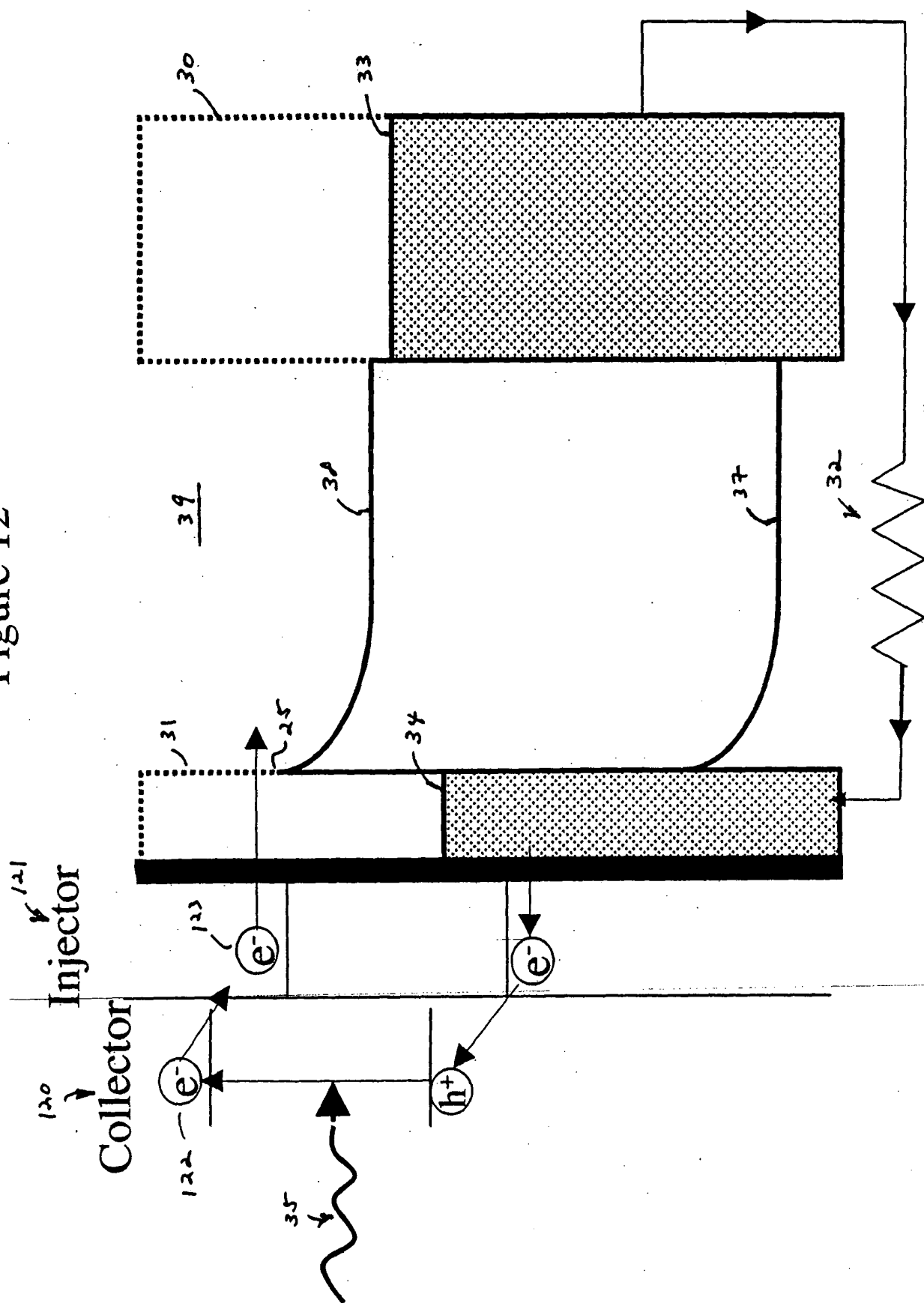


Figure 13

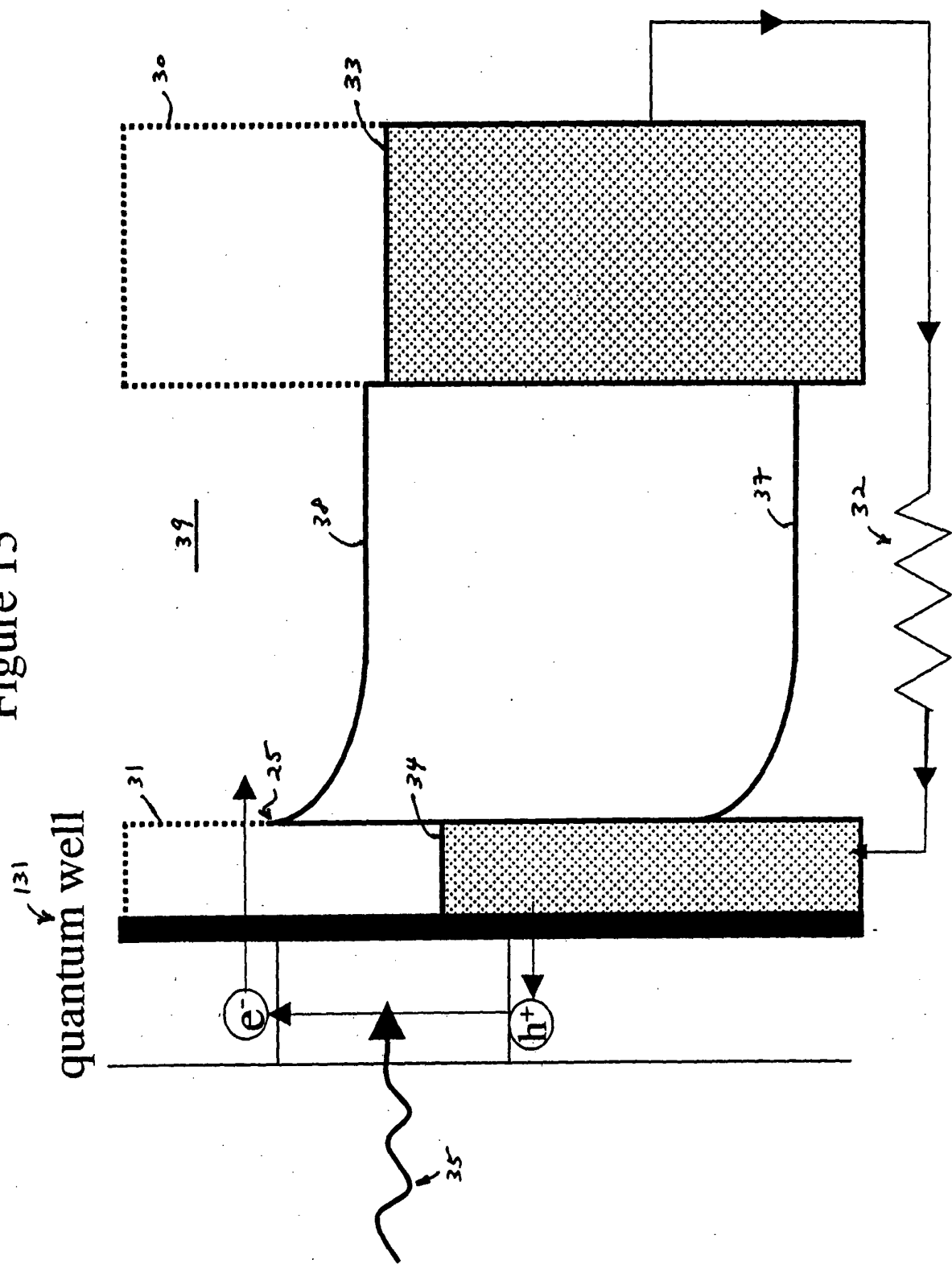


Figure 14

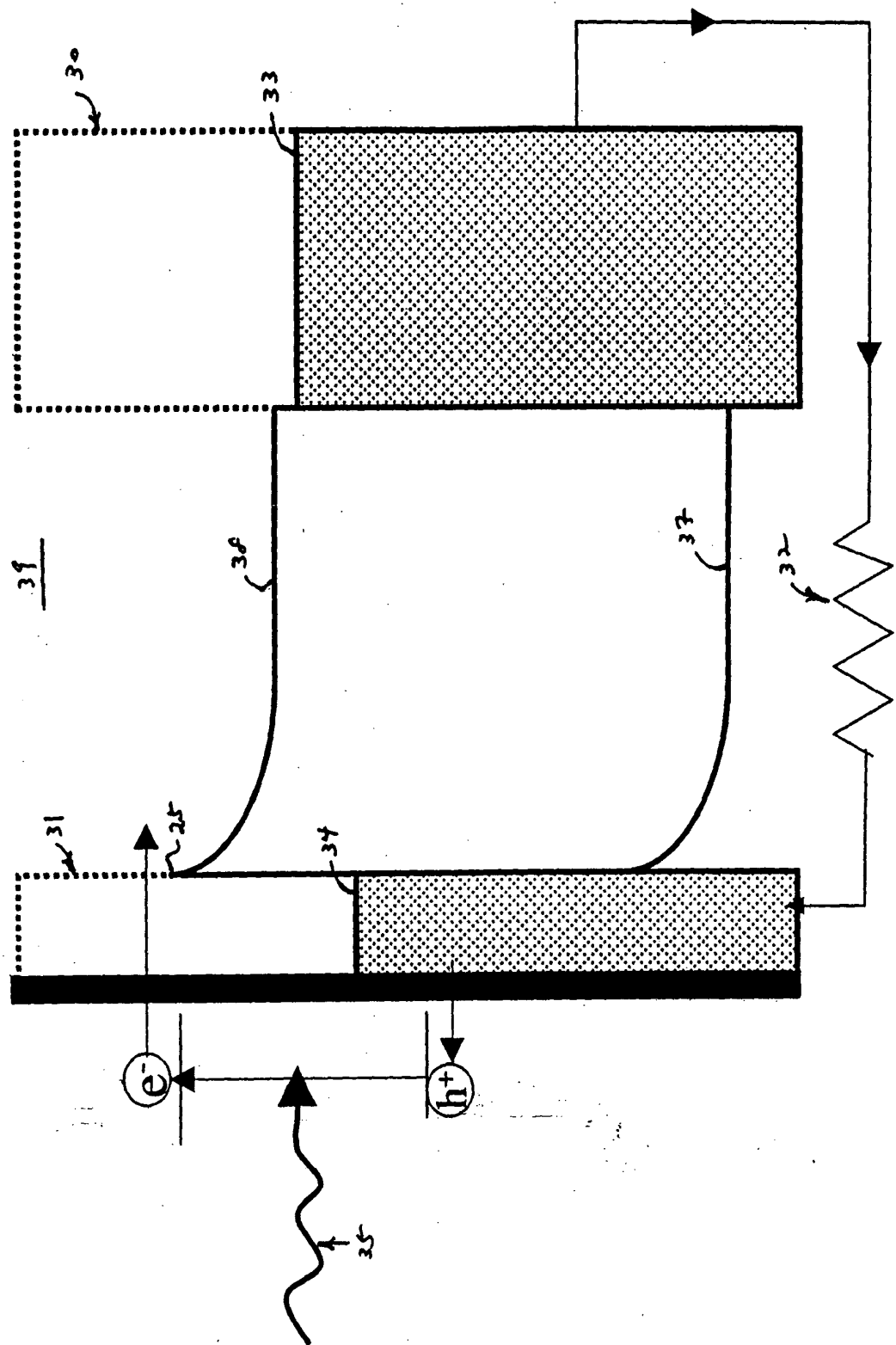


Figure 15

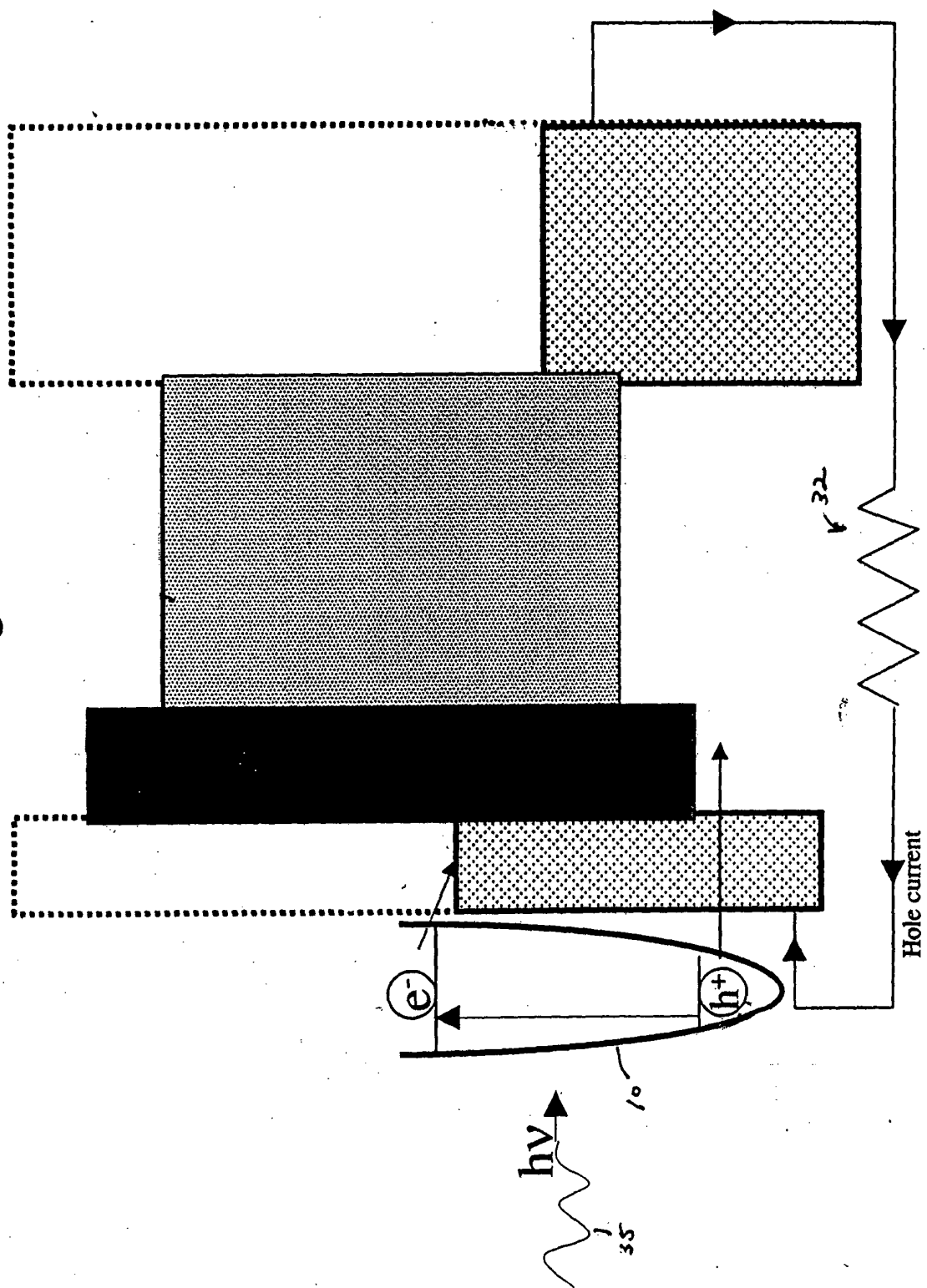


Figure 16: Surface Structures to increase absorption

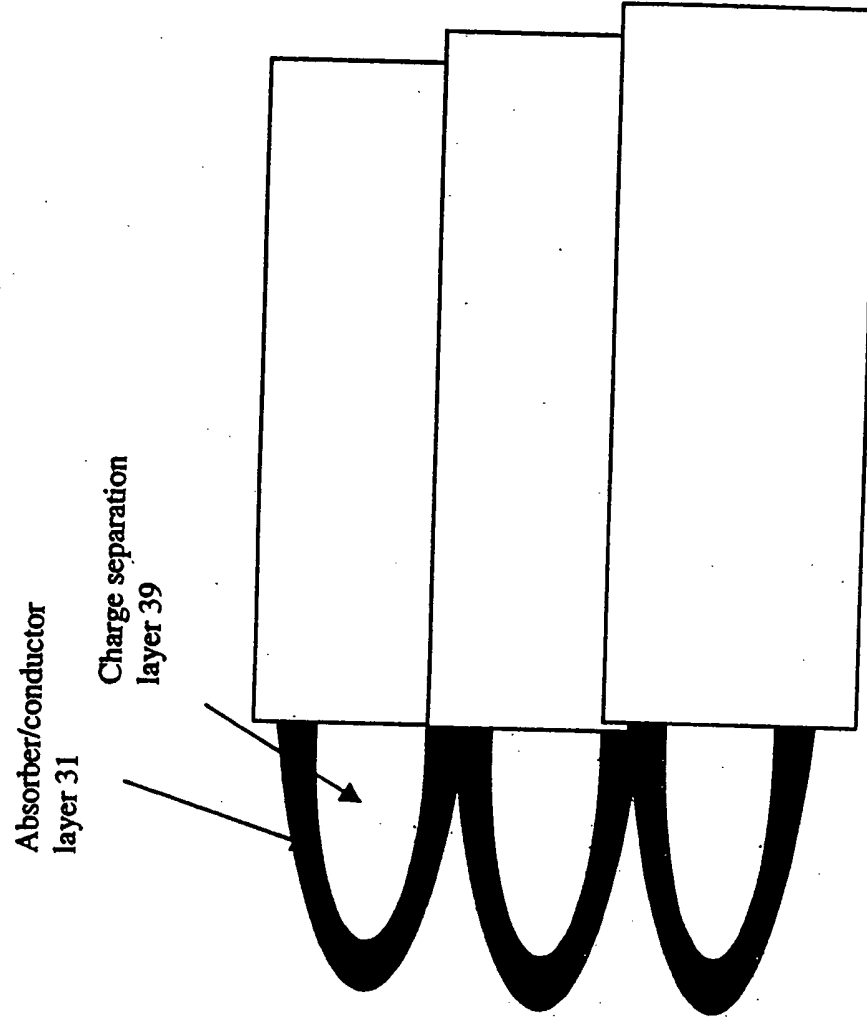




Figure 17

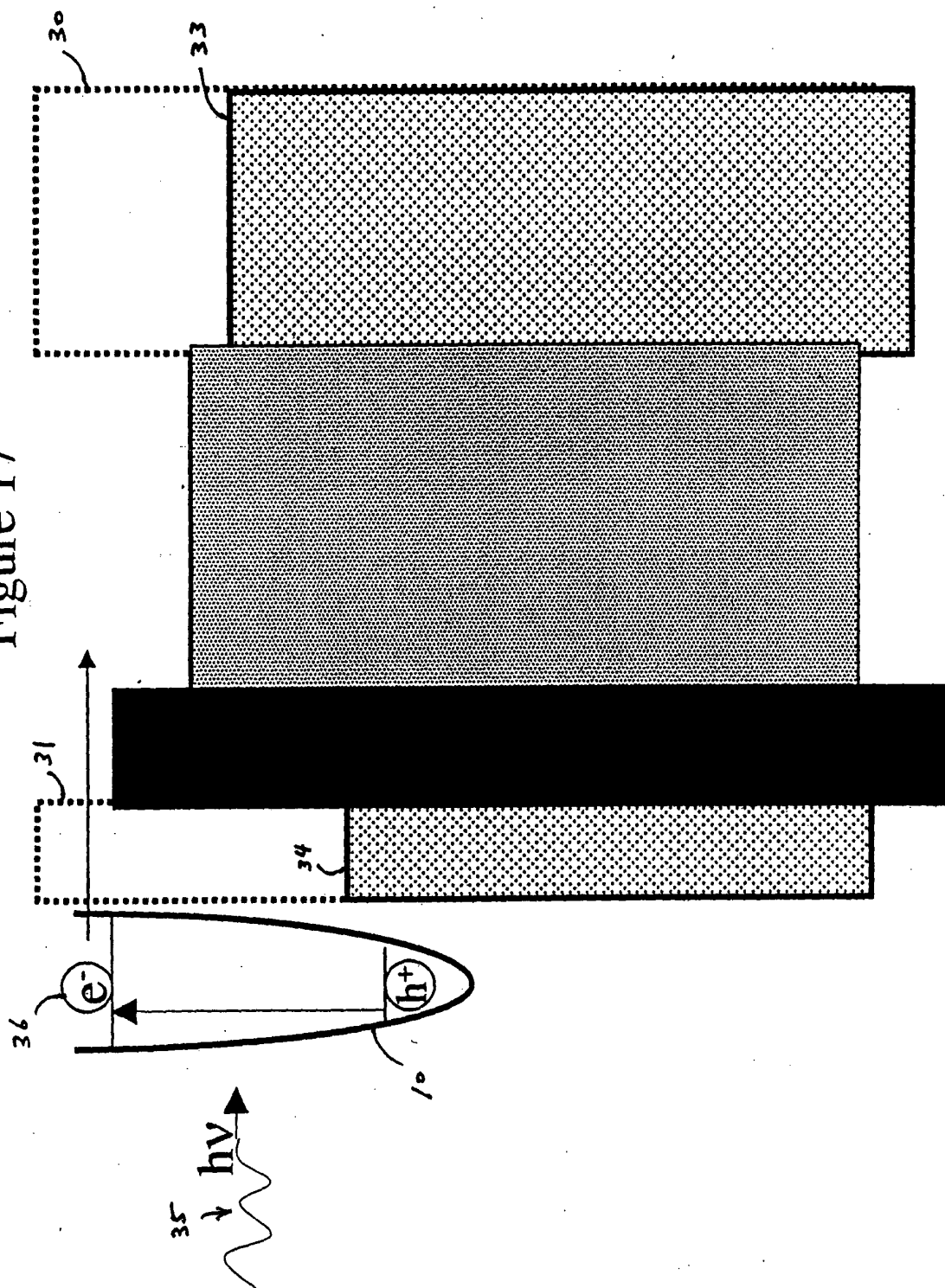


Figure 18: Current Voltage Plots of Device Using Au and Ni ultra-thin conducting layers

